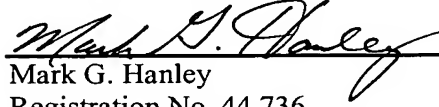


PATENT
Docket No. 20059/PIA31194

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant: Cheolsoo Park)	I hereby certify that this paper is
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Semiconductor Device")	1450 on this date:
)	
Group Art Unit: Unknown)	February 27, 2004
)	
Examiner: Unknown)	
)	Mark G. Hanley
)	Registration No. 44,736
)	Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

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Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

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Correspondence Address:

Respectfully submitted,

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February 27, 2004

Form PTO-1449 (Modified)

U.S. Department of Commerce
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20059/PIA31194

10/749,485

Applicant

Cheolsoo Park

Filing Date

December 30, 2003

Group Art Unit

Unknown

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,130,102	10/10/2000	White, Jr. et al.			
	A02	6,448,649	9/10/2002	Lee et al.			
	A03						
	A04						
	A05						
	A06						
	A07						
	A08						
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	A19						
	A20						
	A21						
	A22						

EXAMINER

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